Studying Crosstalk Trends for Signal Integrity on Interconnects using Finite Element Modeling

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Introduction: High-speed digital design involves electromagnetic coupling between strong adjacent transmission lines. Crosstalk as it is called, has repercussions on signal integrity. The objective is to analyze its effect on victim and observe its trends with changes in sub-domain properties.

Results: Crosstalk trends were observed with respect to 4 parameters – Dielectric Constant of substrate, Height of Substrate, Pitch-Ratio and Ground Planes.







Figure 1. Transmission Lines with Electromagnetic-Coupling

Computational Methods: Modeled the interconnects using COMSOL Multiphsysics 3.5 AC/DC Module. Extracted per-unit length impedance matrices. Crosstalk magnitudes given

Figure 3. Crosstalk variation with Dielectric Constant, Height of substrate, Pitch-Ratio and Grounding Arrangement



by:

$$A_{near_end} = \frac{V_{input}}{4} \left[\frac{L_{M}}{L} + \frac{C_{M}}{C} \right]$$

$$B_{far_end} = -\frac{V_{input} X \sqrt{LC}}{2T_r} \begin{bmatrix} L_M - C_M \\ L & C \end{bmatrix}$$

Geometry: Two transmission lines sitting on a dielectric with dimensions as shown. Both the line have a length of 10 cm each which goes into the plane.



Figure 4. Grounding arrangements. Blue denotes the ground.

Conclusions: To minimize the crosstalk, we must use a low-k dielectric and thin substrate for stronger coupling with ground. Further larger pitch-ratio and orthogonal grounds inserted between the transmission lines substantial reduction in crosstalk ensure because of the shielding and weak coupling.

References:

- SM Musa, NMO Sadiku, Analysis of Multiconductor Quasi-TEM Transmission Lines and Multimode waveguides", Excerpt from the Proceedings of the COMSOL Conference 2010, Boston
- Ashok Goel, "High Speed VLSI Interconnection: Modeling, Analysis and Simulation", Wiley series in Microwave and Optical Engineering.
- Stephen Hall, Garrett Hall, James McCall "High Speed 3. Digital System Design: A handbook of interconnect theory and Design Practices", A Wiley Interscience Publication.

Figure 2. Single Level Interconnections for crosstalk analysis

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